

FIG. 1

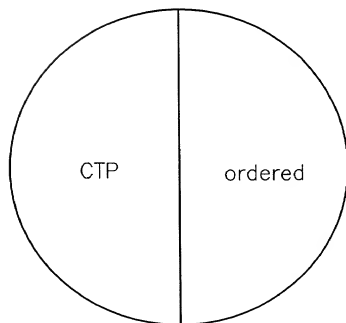


FIG. 2

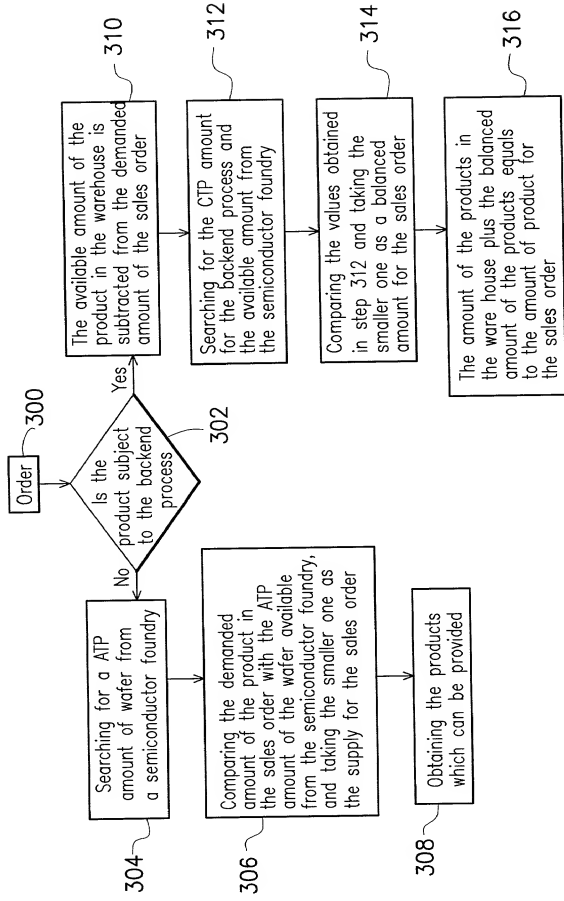


FIG. 3

SEMICONDUCTOR FOUNDRY

BACKEND PROCESS

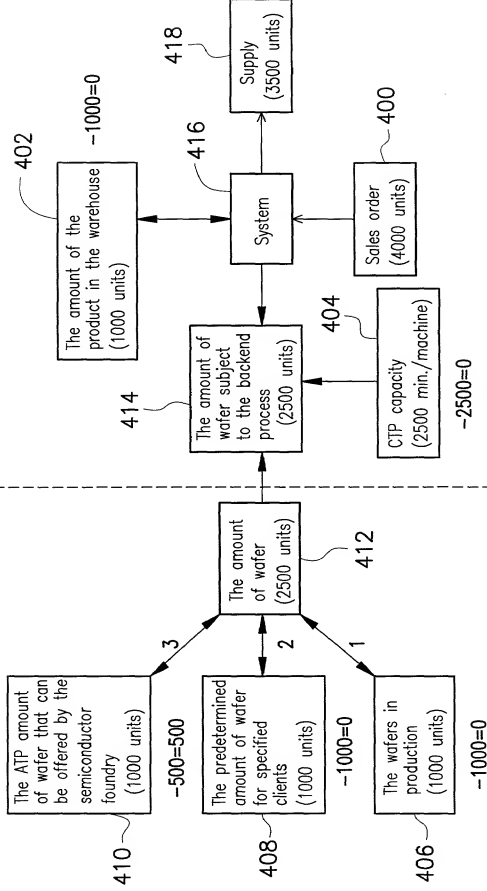


FIG. 4

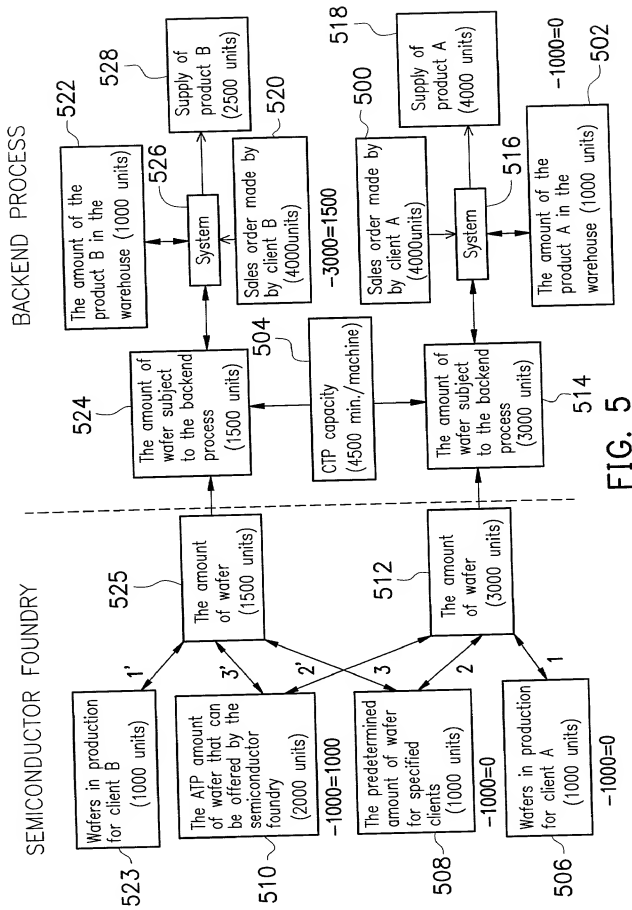


FIG. 5